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MS/2182

Technical documents relating to KDF9 - an early British computer designed and built by English Electric

Compiled by Brian Wichmann

| Item number | Title | Date | Author | Pages |
|----------------|---|---|------------------|--|
| MS/2182/01 | The KDF9 Paper Tape Reader | 27/03/61 | R. T. Shaw | 32 |
| MS/2182/02 | KDF9 - Transformer Sequence Units | 15/11/62 | J. R. Lucking | 19 |
| MS/2182/03 | An Introduction to the KDF9 Computer | 09/08/62 | J. A. Edwards | 23 |
| MS/2182/04 | Main Control Sequence Charts - KDF9 Computer | Last correctio n dates are typically in 1963 | | Ring binder, with no cover sheet contain ing 60- 70 sheets |
| MS/2182/05 | Programmes to Investigate Timings on KDF9 | 10/06/63 | Miss P. Fell | 5 |
| MS/2182/06 | Corrections to Report No. K/GD. Y. 99 "The KDF9 Multiplier - Divider Unit Part A" | 12/08/63 | J. R. Ellison | 3 |
| MS/2182/07 | The Input Output Control of KDF9 | 29/04/63 | D. J. Caton | 34 |
| MS/2182/08 | The KDF9 Magnetic Tape buffer | 26/04/63 | A. Robinson | 17 |

| MS/2182/09 | List of Abbreviation s used in KDF9 Peripheral Transfer Equipment | 04/07/61 | A. W. Nicholson | 15 |
|------------|---|----------|-----------------------------------|--|
| MS/2182/10 | Interruption Flexowriter for KDF9 | 07/09/60 | A. W. Nicholson | 20 |
| MS/2182/11 | The distinction between 'Unready, Busy and Inoperable' in KDF9 | 19/01/61 | A. W. Nicholson | 4 |
| MS/2182/12 | Floating Point Arithmetic in KDF9 | 06/12/62 | J. R. Lucking, J. P. O'Neil | 30 |
| MS/2182/13 | Notes on KDF9 Main Control | 07/09/62 | J. A. Edwards | 40 |
| MS/2182/14 | Figures Relating to Notes on KDF9 Main Control | | | 34 Sheets |
| MS/2182/15 | The KDF9 Multiplier- Divider Unit Part A | 17/06/63 | J. R. Ellison | 51 |
| MS/2182/16 | The KDF9 Paper Tape Punch buffer | 15/05/63 | D. J. Caton | 7 |
| MS/2182/17 | KDF9 Workbook | June 63 | | 96 loose leaf sheets, fastene d |

| | | | | togethe r with two metal rings |
|------------|--|----------|-----------------------------------|--|
| MS/2182/18 | Notes on KDF9 Arithmetic Control | 28/09/62 | J. A. Edwards | 17 |
| MS/2182/19 | Shifting and Shift Control | 10/12/62 | J. R. Lucking, J. P. O'Neil | 25 |
| MS/2182/20 | Basic organisation of controls: and instruction timings | 09/07/62 | J. R. Lucking, J. P. O'Neil | 38 |
| MS/2182/22 | List of Abbreviated Names and Terms used in KDF9 logic | 25/06/62 | J. P. O'Neil | 19 |
| MS/2182/23 | Highest Priority Ready Peripheral Buffer Address Selector | 25/07/63 | A. W. Nicholson | 4 |
| MS/2182/24 | The KDF9 Main store | 24/08/62 | J. A. Edwards | 32 |
| MS/2182/25 | Interruption and Fetch Next Instruction Sequences of the KDF9 Main Control | 01/07/62 | J. R. Lucking | 24 |
| MS/2182/26 | Around 40 circuit diagrams | Undated | | About 40 |

Object no. 2016-418/1